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Pat. No. 05961623 - 5
Issue Date: 06/07/04

Group ID: A
User ID: Tegant

Page 1
KS: 3,725

Warning [US Reference: Page:12 Posn:2]

has an issue date that does not match the patent number

Warning [Pages Of US References:]

page 6 has no references

page 8 has no references

page 10 has no references

Warning [Pages Of Foreign References:]

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Warning [Pages Of Other References:]

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page 1 has no references

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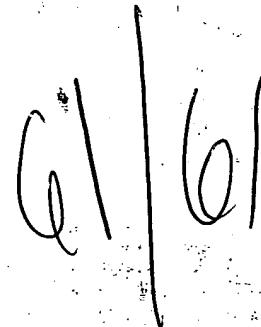
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page 12 has no references





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Rule 47 Continuing Data PCT Disclaimer

NO Yes NO NO

Microfiche Appendix CPA tag

NO NO

Foreign Priority Claimed: NO

Acknowledged: NO

State Code: OR Country Code:

Text Endorsement: 09473575.122899

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JACKET

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>CLASS</u>	<u>SUBCLASS</u>	<u>GAU</u>
09/473,575	12/28/99	718	104	2127

FOREIGN PRIORITY

Country Document Number Date

DISCLAIMER

/ /

TITLE

Method and apparatus for managing resources in a multithreaded processor

MICROFICHE APPENDIX

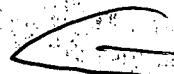
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First: 
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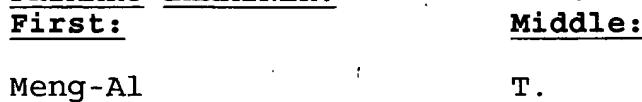
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PRIMARY EXAMINER:

First: 
Meng-Al

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CLAIMS ALLOWED
Total Print

15 1

DRAWINGS

Sheets Figures Print

19 19 Y

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BLUE SLIP INFORMATION

<u>SERIAL NUMBER</u>	<u>CLASS</u>	<u>SUBCLASS</u>	<u>GAU</u>
09/473,575	718	104	2127

INDEP. CLAIMS

TOTAL CLAIMS

1 15

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BLUE SLIP (Page 1)

INTERNATIONAL CLASSIFICATION
Class SubClass

G06F 9/46

CROSS-REFERENCES
Class SubClass

714 100;102;107

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TERM EXTENSION

0

FIELD OF SEARCH

Class SubClass

709 107;100;104

712 239;235;217;227;244;219;42;214;36

718 102,100,104

711 202

OATH

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PCT INFO

CONTINUING DATA (Page 1)

<u>LINE</u>	<u>CODE</u>	<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>STATUS</u>	<u>DOCUMENT NO.</u>	<u>ISSUE DATE</u>
104	72	09/458,544	12/29/1999	01	6,496,925	/ /

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